Chris John Myers

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Education

Stanford University, Stanford, California (1991-1995)

Ph.D. degree in Electrical Engineering (1995)
Thesis: Computer-Aided Synthesis and Verification of Gate-Level Timed Circuits

M.S. degree in Electrical Engineering (1993)

California Institute of Technology, Pasadena, California (1987-1991)

B.S. degree with honor in Electrical Engineering and History (1991)

Professional Experience

Chair of Elec., Comp., and Energy Eng., *University of Colorado Boulder* (2020-present)
Professor of Elec., Comp., and Energy Eng., *University of Colorado Boulder* (2020-present)
Affiliated Professor of Biomedical Engineering, *University of Colorado Boulder* (2020-present)
Affiliated Professor of Computer Science, *University of Colorado Boulder* (2020-present)
Adjunct Professor of Electrical and Computer Eng., *University of Utah* (2021-present)
Adjunct Professor of Computer Science, *University of Utah* (2007-present)
Adjunct Professor of Bioengineering, *University of Utah* (2007-present)
Professor of Electrical and Computer Engineering, *University of Utah* (2006-2021)
Associate Chair of Electrical and Computer Engineering, *University of Utah* (2018-2020)
Visiting Professor of Engineering Science, *University of Oxford* (summer 2019)
Visiting Professor of Electrical and Computer Engineering, *Boston University* (2017)
Visiting Professor of Computer Science, *Newcastle University* (2016-2017)
Director of Computer Engineering, *University of Utah* (2000-2002, 2006-2009)
Adjunct Associate Professor of Computer Science, *University of Utah* (2003-2007)
Adjunct Associate Professor of Bioengineering, *University of Utah* (2003-2007)
Associate Professor of Electrical and Computer Eng., *University of Utah* (2001-2006)
Visiting Scholar, *Tokyo Institute of Technology* (summer 2003)
Visiting Scholar, *Stanford University* (2002-2003)
Research Assistant Professor of Computer Science, *University of Utah* (1996-2003)
Director of the Center for Asynchronous Circuit Design, *University of Utah* (1997-2001)
Assistant Professor of Electrical Engineering, *University of Utah* (1995-2001)
Consultant, Intel Corporation, Portland, Oregon, (1996-1998)
Consultant, Intel Corporation, Haifa, Israel, (summer 1995)
Research Assistant, Professor Teresa Meng, *Stanford University* (1991-1995)
Teaching Assistant, Professor Charles Seitz, *California Institute of Technology* (1990-1991)
Research Assistant, Professor James Lee, *California Institute of Technology* (1989-1991)
Software Engineer, Business Data Systems, Billings, Montana (1985-1989)

Awards

Fellow of the IEEE (2013)
ECE Departmental Service Award, *University of Utah* (2013)
Best Paper Award, Symposium on Asynchronous Circuits and Systems (2007)
Best Paper Award Finalist, Symposium on Asynchronous Circuits and Systems (2001)
Best Paper Award, Symposium Asynchronous Circuits and Systems (1999)
Teaching commendation for CS/EE 5740, *University of Utah* (1998)
National Science Foundation CAREER Award (1996)
National Science Foundation Graduate Fellowship (1991)
Rodman W. Paul History Prize, *California Institute of Technology* (1991)
Tau Beta Pi National Honor Society (1991)
Carnation Merit Award, *California Institute of Technology* (1990)
First Prize VLSI Design Contest, *California Institute of Technology* (1990)

Grant Activities

1. C. Myers, "CHARMME: Center for Harnessing Microbiota from Military Environments", September 2022 - September 2027, Army Research Organization (sub-award from MIT) $750,000.
2. C. Myers, "EAGER: Accelerating Synthetic Biology Discovery through Integrated Curation", August 2022 - July 2024, National Science Foundation, $300,000.
3. C. Myers, "SynBioHub3 - An Interactive Genetic Design Repository", October 2021 - September 2023, National Institute of Standards and Technology, $179,975.
4. C. Myers, "Accelerating Synthetic Biology Discovery & Exploration through Knowledge Integration", October 2019 - September 2021, National Science Foundation, $204,296.
5. C. Myers, "An Efficient Framework for the Stochastic Verification of Computation and Communication Systems Using Emerging Technologies", July 2019 - June 2023, National Science Foundation, $346,000.
6. C. Myers, "Genetic circuit design for extreme environments enabled by models extracted from petabyte+ perturbation analyses", July 2018 - August 2021, DARPA, $308,353.
7. C. Myers, "NSF Student Travel Grant for 2018 Computational Modeling in Biology Network (COMBINE) Forum", May 2018 - May 2019, National Science Foundation, $10,000.
8. C. Myers, "NSF Student Travel Grant for 2018 Hackathons on Resources for Modeling in Biology (HARMONY)", May 2018 - May 2019, National Science Foundation, $10,000.
9. C. Myers, "Evolvable Living Computing - Understanding and Quantifying Synthetic Biological Systems' Applicability, Performance, and Limits", August 2017 - August 2018, National Science Foundation (sub-award from Boston University), $41,266.
10. C. Myers, "A Standard Enabled Workflow for Synthetic Biology", August 2017 - August 2019, National Science Foundation, $150,000, REU supplements totaling $31,758.
11. C. Myers, "Student Travel Support for COMBINE 2016", June 2016 - May 2017, National Science Foundation, $15,000.
12. C. Myers, "Student Travel Support for SBOL 14 Workshop", March 2016 - March 2017, National Science Foundation, $10,000.
13. C. Myers, "Student Travel Support for COMBINE 2015", June 2015 - May 2016, National Science Foundation, two awards totally $16,000.

14. C. Myers, H. Sauro (UW), and J. Gennari (UW), "Synthetic Biology Open Language Resource", July 2014 - June 2018, National Science Foundation, $444,527 (Utah share).

15. C. Myers, "Student Travel Support for HARMONY 2015", July 2014 - June 2015, National Science Foundation, $10,000.

16. C. Myers, "Genetic Design Automation", July 2012 - June 2015, National Science Foundation, $449,980, REU supplements totaling $32,000, travel supplement $15,000.

17. C. Myers and P. Li (TAMU), "Integrated Verification, Built-in Self-test, and Tuning for Digitally-Intensive Analog Systems", July 2011 - July 2014, Nat. Sci. Foundation, $224,998 (Utah share).

18. C. Myers, "Simulation Aided Verification of AMS Circuits", May 2010 - January 2011, Intel Corporation, $35,000.

19. C. Myers and H. Zheng (USF), "Methods and Tools for the Verification of Cyber-Physical Systems", September 2009 - August 2012, National Science Foundation, $270,000 (Utah share).

20. C. Myers and C. Winstead (USU), "Soft-Logic Modeling and Design for Synthetic Biology", July 2009 - June 2012, National Science Foundation, $262,000 (Utah share).

21. C. Myers, "Simulation Aided Verification of AMS Circuits", October 2008 - September 2011, Semiconductor Research Corporation, $198,864.

22. C. Myers, "Designing Reliable Systems Using Unreliable Components", July 2007 - June 2009, National Science Foundation, $150,000.

23. C. Myers, "Formal Verification of Analog and Mixed-Signal Circuits", October 2005 - September 2008, Semiconductor Research Corporation, $270,000.

24. C. Myers, "A Principled Mapping of Regulatory Networks to Asynchronous Circuit Models for Stochastic Analysis", September 2003 - August 2006, National Science Foundation, $425,864, REU supplement $5,000.

25. C. Myers, "System-Level Timing Verification with Automatic Abstraction", August 2002 - July 2005, Semiconductor Research Corporation, $270,000.

26. C. Myers, "U.S.-Japan Cooperative Science: Synthesis and Verification of High Performance Timed Circuits and Systems", April 2001 - April 2004, National Science Foundation, $30,000.

27. C. Myers, C. Schlegel (co-PI), R. Harrison (co-PI), "Design Methodology for Mixed Analog/Asynchronous VLSI Implementations of Communications Systems", September 1999 - August 2002, National Science Foundation, $300,000, REU supplement $5,000.

28. C. Myers, "Timing Verification using Automatic Abstraction", July 1999 - June 2002, Semiconductor Research Corporation, $174,000.

29. C. Myers, E. Brunvand (co-PI), "Center for Asynchronous Circuit and System Design", July 1999 - June 2000, State of Utah, $130,000.

30. C. Myers, "Synthesis and Verification of Timed Circuits", August 1997 - July 2000, Semiconductor Research Corporation, $150,000.

31. C. Myers, "Specification and Compilation Techniques for 1 GHz and Beyond", awarded May 1998, Intel Corporation, $20,000.

32. C. Myers, E. Brunvand (co-PI), "Center for Asynchronous Circuit and System Design", July 1998 - June 1999, State of Utah, $115,000.

33. C. Myers, E. Brunvand (co-PI), "Center for Asynchronous Circuit and System Design", July 1997 - June 1998, State of Utah, $100,000.

34. C. Myers, "Design Methods and Tools for Mixed-Timed Systems", July 1996 - June 2000, NSF CAREER Award, $210,000, U. of Utah matching grant $5,000, REU supplement $5,000.

Development Activities

1. Cash gift from Intel Corporation for research, Dec 2011, $28,000.
2. Cash gift from CEDA to support FAC workshop, June 2011, $3,000.
3. Cash gift from Intel to support FAC workshop, Dec 2010, $2,000.
4. Cash gift from SRC to support verification review, Dec 2006, $5,000.
5. Several cash gifts from SRC to support undergraduate research, May 2006, $6,000, Sept. 2005, $6,000, May 2005, $6,000, Sept. 2004, $6,000, May 2004, $6,000, May 2003, $12,000, Sept. 2002, $12,000, May 2002, $6,000, Aug. 2001, $18,000, May 2001, $12,000, Oct. 2000, $6,000.
6. Cash gift from SRC to support verification review, Dec 2001, $5,000.
7. Computer equipment donation from Intel Corporation, October 2000, valued at $20,000.
8. Software donation from VeriBest Incorporated, January 1999, valued at $2,250,000.
9. Cash gift from Intel Corporation for research, June 1997, $20,000.
10. Software donation from Intel Corporation, July 1996, valued at $1,100.
11. Cash gift from Intel Corporation for research, April 1996, $53,600.
12. Computer equipment donation from Intel Corporation, March 1996, valued at $12,756.
13. Cash gift from Intel Corporation for research, January 1996, $15,500.

Languages

Working knowledge of written and spoken Chinese.

Academic Service

Biomedical Eng. Search Committee Member, *University of Colorado Boulder* (2022-present)
BioFrontiers Council Member, *University of Colorado Boulder* (2020-present)
Computer Engineering Committee Member, *University of Utah* (1995-2016, 2018-2020)
Recruiting Committee Chairman, *University of Utah* (2001-2002, 2004-2006, 2017-2018)
Recruiting Committee Member, *University of Utah* (1995-2000, 2015-2016, 2018-2019)
Coordinator for Electrical Engineering Senior Projects, *University of Utah* (2011-2013)
Faculty Senate, *University of Utah* (2010-2013)
Computer Engineering Director, *University of Utah* (2000-2002, 2006-2009)
Organizer of Judd Distinguished Lecture Series, *University of Utah* (2005-2008)
Assessment and Planning Committee, *University of Utah* (2003-2004)
Strategic Goals Committee Member, *University of Utah* (1997-2001)
Curriculum Committee Member, *University of Utah* (1995-1998)
Computing Committee Member, *University of Utah* (1995-1998)
College Council Member, *University of Utah* (1995-1998)
Alumni Fund House Chair, *California Institute of Technology* (1994-1998)

Professional Activities

Member of the IEEE, S'91-M'96-SM'04-Fellow'13 (1991-present)
Member of the ACM (1996-present)
Chair, *Synthetic Biology Open Language* (2019-present)
Chair, *External Advisory Board for the BioDesign CDT, Imperial College London* (2019-present)
Member of the Editorial Board, *Engineering Biology* (2016-present)
Member of the Editorial Board, *Synthetic Biology* (2016-present)
Member of the Steering Committee, *Synthetic Biology Open Language* (2015-present)
Member, COmputational Modeling in Biology NEtworks Coord. Board (2014-present)
Int. Rep. on the SynBio CDT Directorate Comm., *Oxford University* (2016-2022)
Member of the Portabolomics Advisory Board, *Newcastle University* (2016-2022)
Member of the Organizing Committee for SEED 2022 (2021-2022)
Chair, *COmputational Modeling in Biology NEtworks Coordination Board* (2019-2021)
Member of the Steering Committee, *Synthetic Biology Standards Consortium* (2015-2020)
Member of the Steering Committee, *Frontiers in Analog CAD Workshop* (2010-2018)
Associate Editor, *IEEE Life Sciences Letters*, (2014-2017)
Guest Editor, *IEEE Design & Test Magazine* (2015-2016)
Organizer, *COMBINE Forum 2015* (2015)
Member of the Editorial Board, *Frontiers in Synthetic Biology* (2013-2015)
Associate Editor, *IEEE Transactions on VLSI* (2009-2014)
Member of the Editorial Board, *Formal Methods in System Design* (2006-2014)
Guest Editor, *ACM Journal of Emerging Technologies in Computing Systems*, (2013-2014)
Associate Editor, *IEEE Design and Test Magazine*, (2012-2014)

Editor, *Systems Biology Markup Language* (SBML) (2011-2013)
Guest Editor, *ACS Synthetic Biology*, (2013)
Member of the Review Panel, *NIH MABS Study Section* (2012)
Simulation and Verification Track Chair, *Int. Conf. on Computer-Aided Design* (2012)
Publications Chair, *Int. Workshop on Bio-Design Automation* (2012)
Co-Program Chair, *Frontiers in Analog CAD Workshop* (2011)
Site Reviewer, *NSF Expeditions* (2011)
Member of the Best Paper Committee, *Async. Circuits and Systems Conf.* (2006, 2008)
Organizer, *SRC Verification Review* (2002, 2007)
Co-organizer and Technical Program Chair, *Asynchronous Circuits and Systems Conf.* (2001)
Co-organizer and Technical Program Chair, *Advanced Research in VLSI Conf.* (2001)
Tools Chair, *Asynchronous Circuits and Systems Conference* (1998)
Member of the PC, *Async Ckts/Sys Conf.* (1998-2004, 2006, 2008-2009, 2011, 2017-present)
Member of the PC, *Conference on Analysis and Design of Hybrid Systems* (2017-present)
Member of the PC, *Winter Simulation Conference* (2017-present)
Member of the PC, *Workshop on Hybrid Systems and Biology* (2013-present)
Member of the PC, *Int. Conf. on Formal Modeling and Analysis of Timed Sys.* (2010-present)
Member of the PC, *Int. Workshop on Bio-Design Automation* (2009-present)
Member of the PC, *Frontiers in Analog CAD Workshop* (2005-present)
Member of the PC, *ACM Int. Conf. on Nanoscale Comp. and Comm.* (2016, 2018)
Member of the PC, *IJCAI 2016 Workshop: AI for Synthetic Biology* (2015-2016)
Member of the PC, *Symposium on Theory of Modeling and Simulation* (2014-2017)
Member of the PC, *Forum on Specification and Design Languages* (2011-2016)
Member of the PC, *Workshop on Modeling of Biological Systems*, (2013)
Member of the PC, *ICNC Workshop on Cyber-Physical Systems* (2013)
Member of the PC, *ISMB SIG on Biological System Design* (2012)
Member of the PC, *Int. Conf. on Bioinfo., Biocomp. Sys., and BioTech.* (2011-2012)
Member of the PC, *Great Lakes Symposium on VLSI* (2010-2012)
Member of the PC, *Int. Conf. on Computer-Aided Design* (2009-2012)
Member of the PC, *Virtual Worldwide Forum on Electronic Design Automation*, (2011)
Member of the PC, *Logic Aspects of Fault Tolerance*, (2009, 2011)
Member of the PC, *Int. Conf. on Computational and Systems Biology* (2010)
Member of the PC, *Int. Conf. on Information Technology* (2007)
Member of the PC, *Conf. on Information Technology* (2006)
Member of the PC, *Formal Methods for GALS* (2003, 2005)
Member of the PC, *Computer Aided Verification Conference* (2003-2004)
Member of the PC, *Workshop on Theory and Practice of Timed Systems* (2002)
Panelist, numerous *NSF Review Panels*
Reviewer, *Morgan Kaufman, McGraw Hill, Wiley, Hong Kong Univ. Grants, many IEEE journals*

Graduated Students (Committee Chairman)

PhD - Pedro Fontanarrosa - Auto. Gen. of Dynamic Models for Genetic Regulatory Net., 8/22.
PhD - Jeanet Mante - Promotion of Data Reuse in Synthetic Biology, 5/22.
PhD - Tramy Nguyen - Asynchronous Genetic Circuit Design, 9/19.
PhD - Leandro Watanabe - Modeling and Simulation for Heterogeneous Populations, 12/18.
PhD - Zhen Zhang - Verification Methodologies for Fault-Tolerant NoC Systems, 12/15.
PhD - Andrew Fisher - Efficient, Sound Formal Verification for AMS Circuits, 8/15.
PhD - Nicholas Roehner - Technology Mapping of Genetic Circuit Designs, 8/14.
PhD - Curtis Madsen - Stochastic Analysis of Synthetic Genetic Circuits, 8/13.
PhD - Robert Thacker - A New Verification Method For Embedded Systems, 12/10.
PhD - Scott Little - Efficient Modeling and Verif. of AMS Circuits Using LHPNs, 8/08.
PhD - Nathan Barker - Learning Genetic Reg. Net. Connectivity from Time Series Data, 8/07.
PhD - Hiroyuki Kuwahara - Model Abs. & Temp. Behv. Analysis of Genetic Reg. Net., 8/07.
PhD - David Walter - Verification of AMS Circuits Using Symbolic Methods, 8/07.
PhD - Curt Nelson - Technology Mapping of Timed Cirucits, 12/04.
PhD - Hans Jacobson - Interlocked Synchronous Pipelines, 12/03.
PhD - Eric Mercer - Correctness and Reduction in Timed Circuit Analysis, 12/02.
PhD - Jie Dai - Design Methodology for Analog VLSI Impl. of Error Control Decoders, 12/02.
PhD - Eric Peskin - Protocol Selection, Impl., and Analysis for Asynchronous Circuits, 8/02.
PhD - Hao Zheng - Modular Syn.&Verif. of Timed Circuits Using Automatic Abstraction, 8/01.
PhD - Wendy Belluomini - Algorithms for Synthesis and Verification of Timed Circuits, 9/99.
MS - Logan Terry, SBOLCanvas: A Visual Editor for Genetic Designs, 5/21.
MS - Pedro Fontanarrosa, Automated Generation of Dynamic Models for GRNs, 6/19.
MS - Michael Zhang, SBOLExplorer: Data Mining for Genetic Design Repositories, 4/19.
MS - Meher Samineni, Software Compliance Testing for Workflows using SBOL, 8/18.
MS - Dhanashree Kulkarni, Improved Model Gen. and Prop. Spec. for AMS Circuits, 8/13.
MS - Satish Batchu - Automatic Extraction of Behv. Models from Sim. of AMS Circuits, 12/10.
MS - Nam Nguyen - Design and Analysis of Genetic Circuits, 8/08.
MS - Yanyi Zhao - Application of Sync Synthesis Tools for High-Level Async Design, 12/04.
MS - Chris Krieger - Complete State Coding of Timed Asynchronous Circuits, 12/02.
MS - Kip Killpack - Analysis and Characterization of a Locally-Clocked Module, 5/02.
MS - Robert Thacker - Implicit Methods for Timed Circuit Synthesis, 6/98.
MS - Hao Zheng - Specification and Compilation of Timed Systems, 6/98.
MS - Brandon Bachman - Architectural-Level Synthesis for Asynchronous Systems, 9/98.
MS - Eric Mercer - Stochastic Cycle Period Analysis in Timed Circuits, 5/99.
BS - Eric Yu, SynBioHub3 Back-End, 5/22.
BS - Michael Zhang, SBOLDesigner: A Hierarchical Genetic Design Editor, 4/18.
BS - Meher Samineni, Software Compliance Testing for SBOL, 8/17.
BS - Leandro Watanabe - Hierarchical Stochastic Simulation Algorithm, 5/14.
BS - Tyler Patterson - Modeling and Visualization of Genetic Circuits, 5/11.
BS - Kevin Jones - Automated Abstraction of Labeled Petri Nets, 5/11.
BS - Scott Little - Comparative Study of Several Timing Analysis Algorithms, 5/03.
BS - Yanyi Zhao - An Aysnchronous MPEG Ditherer, 5/03.
BS - Allen Sjogren - Interfacing Synchronous and Asynchronous Modules, 5/98.
BS - Jeff Cuthbert - XATACS: The Next Generation in Asynchronous Circuit Design, 5/98.

Current Graduate Students (Committee Chairman)

PhD - Daniel Fang, Computer Science, University of Colorado Boulder
PhD - Lukas Bucherl, Biomedical Engineering, University of Colorado Boulder
PhD - Sai Samineni, Biomedical Engineering, University of Colorado Boulder

Postdocs Supervised

Pedro Fontanarrosa - Characterization and Modeling of Genetic Parts (2022-present)
Jeanet Mante - Integrated Curation for Synthetic Biology (2022-2023)
Zhen Zhang - libSBOLj: A Java Library for SBOL (2015-2016)
Jian Wu - Analysis of Network-on-Chip Routing Protocols (2010-2011)
Sung-tae Jung - Direct Synthesis of Timed Circuits (1999-2000)

Undergraduate Projects Supervised

William Dravenstott - Excel2SBOL Converter (2022-current)
Anjala Katuri - SynBioHub3 Testing (2022-current)
Zane Perry - SynBioHub3 Plugins (2022-current)
Payton Thomas - iBioSim (2020-current)
Benjamin Hatch - VisBOL and SynBioHub3 (2019-current)
Zachary Sents - SynBioSuite (2022-2023)
Thomas Stoughton - iBioSim Server (2021-2022)
Julian Abam - ExcelSBOL Converter (2021-2022)
Eric Yu - SBOLExplorer and SynBioHub3 (2019-2022)
James Scholz - SynBioHub Testing (2019-2021)
CS Capstone Design Team (2019-2020)
Oliver Flatt - SynBioHub Testing
Samuel Bridge - SBOLDesigner
Nathan Wilkinson - VisBol
Igor Durovic - SBOL Library
Zach Zundel - SynBioHub
Tramy Nguyen - SBOL Library
Scott Glass - Flux Balance Analysis
Jason Stevens - Dynamic Modeling of Genetic Circuits
Hill Air Force clinic (2003-2008)
Curtis Madsen - Engineering Scholars Program
Nick Seegmiller - Engineering Scholars Program
Kip Killpack - A Low Power Digital Hearing Aid
Eric Mercer - SPAM Microprocessor Simulator
Brandon Bachman - SPAM Microprocessor Simulator
Robert Thacker - SPAM Microprocessor

Courses Taught

Engineering Genetic Circuits (2021,2022)

Modeling and Analysis of Biological Networks (2003,2005,2007,2010,2013,2015,2017,2018)

Computer Design Laboratory (1999,2001,2012,2014,2019)

Fundamentals of Digital System Design (1999,2001,2014,2016,2019)

Asynchronous Circuit Design (1995,1997,2000,2001,2004,2006,2009,2011,2013,2015)

Formal Verification (1997,2012)

Embedded System Design (2004-2007, 2011)

Case Studies in CES (2008)

CE Junior Seminar (2006-2008,2018)

Hill Air Force Clinic (2003-2008)

CE Senior Thesis (2002)

Computer Aided Design of Digital Circuits (1996,1998,2000)

Hardware Fundamentals: Computer Organization and Design (1997-1998)

Microprocessor Laboratory (1996)

Courses Developed

1. Modeling and Analysis of Biological Networks - In 2009, I published the textbook, *Engineering Genetic Circuits*, used for this course.
2. Asynchronous Circuit Design - In 2001, I published the textbook, *Asynchronous Circuit Design*, used for this course.
3. Computer Aided Design of Digital Circuits - This course provides an introduction to algorithms for the synthesis and optimization of digital designs.
4. Embedded System Design - I completely redesigned this course to focus on embedded system design issues rather than just interfacing with a PC.
5. Hardware Fundamentals - I completely redesigned a core course on hardware fundamentals and computer organization including adding new material to teach VHDL.
6. Formal Verification - This course presents state-of-the-art methods for the formal verification of hardware and software systems. A new version of this class was developed in 2012.

Publications and Patents

Books

1. C. J. Myers (translated by Li), *Asynchronous Circuit Design* (in Chinese), Tsinghua University Press, September, 2013.
2. C. J. Myers, *Engineering Genetic Circuits*, Chapman & Hall/CRC Press, July, 2009.
3. C. J. Myers (translated by T. Yoneda), *Asynchronous Circuit Design* (in Japanese), Kyoritsu Shuppan, September, 2003.
4. C. J. Myers, *Asynchronous Circuit Design*, John Wiley and Sons, July, 2001.
5. J. Z. Lee, C. Campbell, and C. J. Myers, *Fate and Fortune in Rural China: Social Structure and Population Behavior in Liaoning*, Cambridge University Press, 1997.

Book Chapters and Books Edited

1. T. Neupane, Z. Zhang, C. Madsen, H. Zheng, and C. Myers, "Approximation techniques for stochastic analysis of biological systems", in *Automated Reasoning for Systems Biology and Medicine*, pages 327-348, 2019.
2. C. Myers, K. Clancy, G. Misirli, E. Oberortner, M. Pocock, J. Quinn, N. Roehner, and H. Sauro, "The Synthetic Biology Open Language", in *Computational Methods in Synthetic Biology*, Methods in Molecular Biology, Volume 1244, pages 323-336, 2015.
3. C. Madsen, C. Myers, N. Roehner, C. Winstead, and Z. Zhang, "Efficient Analysis Methods in Synthetic Biology", in *Computational Methods in Synthetic Biology*, Methods in Molecular Biology, Volume 1244, pages 217-257, 2015.
4. A. Fisher, D. Kulkarni, and C. Myers, "A new assertion property language for analog/mixed-signal circuits", in *Languages, Design Methods, and Tools for Electronic System Design - Selected Contributions from FDL 2013*, Lecture Notes in Electrical Engineering, Volume 311, pages 45-65, 2015.
5. C. Myers, "Platforms for Genetic Design Automation", in *Methods in Microbiology 2013: Microbial Synthetic Biology*, November, 2013.
6. C. Myers (editor), *Stochastic Control*, Sciyo, August, 2010.
7. H. Kuwahara, C. Madsen, I. Mura, C. Myers, A. Tejeda, and C. Winstead, "Efficient stochastic simulation to analyze targeted properties of biological systems," in *Stochastic Control*, Sciyo, pages 505-532, August, 2010.
8. H. Kuwahara and C. J. Myers, "Abstraction methods for analysis of gene regulatory networks," in *Computational Methods in Gene Regulatory Networks*, IGI Global, pages 352-385, 2010.
9. E. Brunvand and C. Myers (editors), *2001 Conference on Advanced Research in VLSI*, IEEE Computer Society, March, 2001.
10. T. Suemei, J. Lee, C. Myers, and C. Campbell, "Machine analysis and data coding of the Qing imperial lineage" (in Chinese), in *The Demographic Behavior and Social Environment of the Qing Imperial Lineage*, Beijing University Press, 1994.

Journal Articles

1. J. Mante, J. Abam, S. Samineni, I. Potzsch, J. Beal, C. Myers, "Excel-SBOL Converter: Creating SBOL from Excel Templates and Vice Versa", in *ACS Synthetic Biology*, January 3, 2023.
2. M. Ahmadi, P. Thomas, L. Buecherl, C. Winstead, C. Myers, H. Zheng, "A Comparison of Weighted Stochastic Simulation Methods for the Analysis of Genetic Circuits", in *ACS Synthetic Biology*, December 30, 2022.
3. R. Goldman, R. Moseley, B. Cummins, J. Vrana, N. Roehner, K. Clowers, D. Bryce, J. Beal, M. DeHaven, J. Nowak, T. Higa, V. Biggers, P. Lee, J. Hunt, L. Mosqueda, S. Haase, M. Weston, G. Zheng, A. Deckard, S. Gopaulakrishnan, J. Stubbs, N. Gaffney, M. Vaugn, N. Maheshri, E. Mihalev, B. Bartley, R. Markeloff, T. Mitchell, T. Nguyen, D. Sumorok, N. Walczak, C. Myers, Z. Zundel, B. Hatch, J. Scholz, J. Colonna-Romano, "Highly-Automated, High-Throughput Replication of Yeast-based Logic Circuit Design Assessments", in *Oxford Synthetic Biology*, 7(1), October 6, 2022.
4. B. Shaikh, L. Smith, D. Vasilescu, G. Marupilla, M. Wilson, E. Agmon, H. Agnew, S. Andrews, A. Anwar, M. Beber, F. Bergmann, D. Brooks, L. Brusch, L. Calzone, K. Choi, J. Cooper, J. Detloff, B. Drawert, M. Dumontier, G. Ermentrout, J. Faeder, A. Freiburger, F. Fröhlich, A. Funahashi, A. Garny, J. Gennari, P. Gleeson, A. Goelzer, Z. Haiman, J. Hasenauer, J. Hellerstein, H. Hermjakob, S. Hoops, J. Ison, D. Jahn, H. Jakubowski, R. Jordan, M. Kalaš, M. König, W. Liebermeister, R. Sheriff, S. Mandal, R. McDougal, J. Medley, P. Mendes, R. Müller, C. Myers, A. Naldi, T. Nguyen, D. Nickerson, B. Olivier, D. Patoliya, L. Paulevé, L. Petzold, A. Priya, A. Rampadarath, J. Rohwer, A. Saglam, D. Singh, A. Sinha, J. Snoep, H. Sorby, R. Spangler, J. Starruß, P. Thomas, D. Niekerk, D. Weindl, F. Zhang, A. Zhukova, A. Goldberg, J. Schaff, M. Blinov, H. Sauro, I. Moraru, J. Karr, "BioSimulators: a central registry of simulation engines and services for recommending specific tools", in *Nucleic Acids Research*, 50(W1):W108–14, May 7, 2022.
5. L. Buecherl and C. Myers, "Engineering Genetic Circuits: Advancements in Genetic Design Automation Tools and Standards for Synthetic Biology", in *Current Opinions in Microbiology*, August, 2022.
6. T. Jones, S. Oliveira, W. Jackson, C. Myers, C. Voigt, and D. Densmore, "Genetic Circuit Design Automation with Cello 2.0", in *Nature Protocols*, 17: 1097-1113, February 23, 2022.
7. D. Bryce, R. Goldman, M. DeHaven, J. Beal, B. Bartley, T. Nguyen, N. Walczak, M. Weston, G. Zheng, J. Nowak, P. Lee, J. Stubbs, N. Gaffney, M. Vaughn, C. Myers, R. Moseley, S. Haase, A. Deckard, B. Cummins, and N. Leiby, "Round Trip: An Automated Pipeline for Experimental Design, Execution, and Analysis" in *ACS Synthetic Biology*, 11(2): 608-622, January 21, 2022.
8. E. Yu, J. Mante, and C. Myers, "Sequence-based Searching for SynBioHub Using VSEARCH", in *ACS Synthetic Biology*, 11(2): 990-995, January 21, 2022.
9. N. Roehner, J. Mante, C. Myers, and J. Beal, "Synthetic Biology Curation Tools (SYNBICT)", in *ACS Synthetic Biology*, 10(11): 3200-3204, November 19, 2021.
10. L. Buecherl, R. Roberts, P. Fontanarrosa, P. Thomas, J. Mante, Z. Zhang, and C. Myers, "Stochastic Hazard Analysis of Genetic Circuits in iBioSim and STAMINA", in *ACS Synthetic Biology*, 10(10): 2532-2540, October 15, 2021.
11. J. Mante, N. Roehner, K. Keating, J. Mclaughlin, E. Young, J. Beal, and C. Myers, "Curation Principles Derived from the Analysis of the SBOL iGEM Data Set", in *ACS Synthetic Biology*, 10(10): 2592-2606, October 15, 2021.
12. J. Mante, Y. Hao, J. Jett, U. Joshi, K. Keating, X. Lu, G. Nakum, N. Rodriguez, J. Tang,

L. Terry, X. Wu, E. Yu, J. Downie, B. McInnes, M. Nguyen, B. Sepulvado, E. Young, and C. Myers, "The Synthetic Biology Knowledge System", in *ACS Synthetic Biology*, 10(9): 2276-2285, September 17, 2021.

13. F. Schreiber, P. Gleeson, M. Golebiewski, T. Gorochowski, M. Hucka, S. Keating, M. Konig, C. Myers, D. Nickerson, B. Sommer, and D. Waltemath, "Specifications of Standards in Systems and Synthetic Biology: Status and Developments in 2021", in *Journal of Integrative Bioinformatics*, 18(3), September, 2021.

14. H. Baig, P. Fontanarossa, J. McLaughlin, J. Scott-Brown, P. Vaidyanathan, T. Gorochowski, G. Misirli, J. Beal, and C. Myers, "Synthetic Biology Open Language Visual (SBOL Visual) Version 3.0", in *Journal of Integrative Bioinformatics*, 18(3), September, 2021.

15. H. Baig, P. Fontanarossa, V. Kulkarni, J. McLaughlin, P. Vaidyanathan, B. Bartley, S. Bhakta, S. Bhatia, M. Bissell, K. Clancy, R. Cox, A. Moreno, T. Gorochowski, R. Grunberg, J. Lee, A. Luna, C. Madsen, G. Misirli, T. Nguyen, N. Le Novere, Z. Palchick, M. Pocock, N. Roehner, H. Sauro, J. Scott-Brown, J. Sexton, G.-B. Stan, J. Tabor, L. Terry, M. Vilar, C. Voigt, A. Wipat, D. Zong, Z. Zundel, J. Beal, and C. Myers, "Synthetic Biology Open Language Visual (SBOL Visual) Version 2.3", in *Journal of Integrative Bioinformatics*, 18(3), September, 2021.

16. B. Hatch, L. Meng, J. Mante, J. McLaughlin, J. Scott-Brown, and C. Myers, "VisBOL2 - Improving Web-based Visualization for Synthetic Biology Designs", in *ACS Synthetic Biology*, 10(8): 2111-2115, August 20, 2021.

17. L. Terry, J. Earl, S. Thayer, S. Bridge, and C. Myers, "SBOLCanvas: A Visual Editor for Genetic Designs", in *ACS Synthetic Biology*, 10(7): 1792-1796, July 16, 2021.

18. K. Kiwimagi, J. Letendre, B. Weinberg, J. Wang, M. Chen, L. Watanabe, C. Myers, J. Beal, W. Wong, and R. Weiss, "Quantitative Characterization of Recombinase-Based Digitizer Circuits Enables Predictable Amplification of Biological Signals", in *Nature Communications Biology*, 4(875), July 15, 2021.

19. P. Fontanarrosa, H. Doosthosseini, A. Borujeni, Y. Dorfan, C. Voigt, and C. Myers, "Genetic Circuit Dynamics: Hazard and Glitch Analysis ", in *ACS Synthetic Biology*, 9(9): 2324-2338, September 18, 2020.

20. J. McLaughlin, J. Beal, G. Misirli, R. Grunberg, B. Bartley, J. Scott-Brown, P. Vaidyanathan, P. Fontanarrosa, E. Oberortner, A. Wipat, T. Gorochowski, C. Myers, "The Synthetic Biology Open Language (SBOL) Version 3: Simplified Data Exchange for Bioengineering", in *Frontiers in Bioengineering and Biotechnology*, 8, September 11, 2020.

21. D. Waltemath, M. Golebiewski, M. Blinov, P. Gleeson, H. Hermjakob, M. Hucka, E. Inau, S. Keating, M. Konig, O. Krebs, R. Malik-Sheriff, D. Nickerson, E, Oberortner, H. Sauro, F. Schreiber, L. Smith, M. Stefan, U. Wittig, and C. Myers, "The first 10 years of the international coordination network for standards in systems and synthetic biology (COMBINE)", in *Journal of Integrative Bioinformatics*, 17(2-3), June 1, 2020.

22. F. Schreiber, B. Sommer, T. Czauderna, M. Golebiewski, T. Gorochowski, M. Hucka, S. Keating, M. Konig, C. Myers, D. Nickerson, and D. Waltemath, "Specifications of Standards in Systems and Synthetic Biology: Status and Developments in 2019", in *Journal of Integrative Bioinformatics*, 17(2-3), June 1, 2020.

23. S. Keating, D. Waltemath, M. Konig, F. Zhang, A. Drager, C. Chaouiya, F. Bergmann, A. Finney, C. Gillespie, T. Helikar, S. Hoops, R. Malik-Sheriff, S. Moodie, I. Moraru, C. Myers, A. Naldi, B. Olivier, S. Sahle, J. Schaff, L. Smith, M. Swat, D. Thieffry, L. Watanabe, D. Wilkinson, M. Blinov, K. Begley, J. Faeder, H. Gomez, T. Hamm, Y. Inagaki, W. Liebermeister,

A. Lister, D. Lucio, E. Mjolsness, C. Proctor, K. Raman, N. Rodriguez, C. Shaffer, B. Shapiro, J. Stelling, N. Swainston, N. Tanimura, J. Wagner, M. Meier-Schellersheim, H. Sauro, B. Palsson, H. Bolouri, H. Kitano, A. Funahashi, H. Henning, J. Doyle, and M. Hucka, "SBML Level 3: an extensible format for the exchange and reuse of biological models", in *Molecular Systems Biology*, 16(3): e9110, August 1, 2020.

24. H. Baig, P. Fontanarrosa, V. Kulkarni, J. McLaughlin, P. Vaidyanathan, B. Bartley, J. Beal, M. Crowther, T. Gorochowski, R. Grunberg, G. Misirli, J. Scott-Brown, E. Oberortner, A. Wipat, and C. Myers, "Synthetic Biology Open Language (SBOL) Version 3.0.0", in *Journal of Integrative Bioinformatics*, 17(2-3), June 1, 2020.

25. H. Baig, P. Fontanarrosa, V. Kulkarni, J. McLaughlin, P. Vaidyanathan, B. Bartley, S. Bhatia, S. Bhakta, M. Bissell, K. Clancy, R. Cox, A. Goni Moreno, T. Gorochowski, R. Grunberg, A. Luna, C. Madsen, G. Misirli, T. Nguyen, N. Le Novere, Z. Palchick, M. Pocock, N. Roehner, H. Sauro, J. Scott-Brown, J. Sexton, G.-B. Stan, J. Tabor, M. Vilar, C. Voigt, A. Wipat, D. Zong, Z. Zundel, J. Beal, and C. Myers, "Synthetic Biology Open Language Visual (SBOL Visual) Version 2.2", in *Journal of Integrative Bioinformatics*, 17(2-3), June 1, 2020.

26. L. Smith, F. Bergmann, C. Gillespie, S. Keating, M. Konig, C. Myers, M. Swat, D. Wilkinson, and M. Hucka, "The Systems Biology Markup Language (SBML) Level 3 package: Distributions, version 1, release 1", in *Journal of Integrative Bioinformatics*, 17(2-3), June 1, 2020.

27. J. Mante, Z. Zundel, and C. Myers, "Extending SynBioHub's Functionality with Plugins", in *ACS Synthetic Biology*, 9(5): 1216-1220, May 15, 2020.

28. J. Beal, A. Goni-Moreno, C. Myers, A. Hecht, M. de Vicente, M. Parco, M. Schmidt, K. Timmis, G. Baldwin, S. Friedrichs, P. Freemont, D. Kiga, E. Ordozgoiti, M. Rennig, L. Rios, K. Tanner, V. de Lorenzo, and M. Porcar, "The long journey towards standards for engineering biosystems", in *EMBO Reports*, e50521, April 26, 2020.

29. G. Misirli, J. Beal, T. Gorochowski, G.-B. Stan, A. Wipat, C. Myers, "SBOL Visual 2 Ontology", in *ACS Synthetic Biology*, 9(4): 972-977, April 17, 2020.

30. M. Zhang, Z. Zundel, C. Myers, "SBOLExplorer: Data Infrastructure and Data Mining for Genetic Design Repositories", in *ACS Synthetic Biology*, 8(10): 2287-2294, September 18, 2019.

31. J. Beal, T. Nguyen, T. Gorochowski, A. Goni-Moreno, J. Scott-Brown, J. McLaughlin, C. Madsen, B. Aleritsch, B. Bartley, S. Bhakta, M. Bissell, S. Castillo-Hair, K. Clancy, A. Luna, N. Le Novere, Z. Palchick, M. Pocock, H. Sauro, J. Sexton, J. Tabor, C. Voigt, Z. Zundel, C. Myers, A. Wipat, "Communicating Structure and Function in Synthetic Biology Diagrams", in *ACS Synthetic Biology*, 8(8): 1818-1824, July 26, 2019.

32. F. Schreiber, B. Sommer, G. Bader, P. Gleeson, M. Golebiewski, M. Hucka, S. Keating, M. Konig, C. Myers, D. Nickerson, and D. Waltemath, "Specifications of Standards in Systems and Synthetic Biology: Status and Developments in 2019", in *Journal of Integrative Bioinformatics*, 16(2), June, 2019.

33. M. Hucka, F. Bergmann, A. Drager, S. Hoops, S. Keating, M. Konig, N. Le Novere, C. Myers, B. Olivier, S. Sahle, J. Schaff, R. Sheriff, L. Smith, D. Waltemath, D. Wilkinson, and F. Zhang, "The Systems Biology Markup Language (SBML): Language Specification for Level 3 Version 2 Core Release 2", in *Journal of Integrative Bioinformatics*, 16(2), June, 2019.

34. C. Madsen, A. Moreno, U. P, Z. Palchick, N. Roehner, A. Wipat, J. Beal, M. Bissell, J. Gennari, H. Sauro, K. Clancy, G. Misirli, C. Myers, E. Oberortner, C. Macklin, Y. Tarter, J. Meng, R. Grunberg, C. Atallah, J. McLaughlin, R. Cox, B. Bartley, M. Pocock, T. Gorochowski, J. Scott-Brown, T. Nguyen, M. Samineni, M. Zhang, Z. Zundel, K. Choi, Z. Zhang, "Synthetic

Biology Open Language (SBOL) Version 2.3", in *Journal of Integrative Bioinformatics*, 16(2), June, 2019.

35. C. Madsen, A. Moreno, Z. Palchick, U. P, N. Roehner, A. Wipat, B. Bartley, J. Beal, S. Bhatia, S. Bhakta, M. Bissell, K. Clancy, R. Cox, T. Gorochowski, R. Grunberg, A. Luna, J. McLaughlin, C. Myers, T. Nguyen, N. Le Novere, M. Pocock, H. Sauro, J. Scott-Brown, J. Sexton, G.-B. Stan, J. Tabor, C. Voigt, Z. Zundel, "Synthetic Biology Open Language Visual (SBOL Visual) Version 2.1", in *Journal of Integrative Bioinformatics*, 16(2), June, 2019.

36. L. Watanabe, J. Barhak, and C. Myers, "Towards Reproducible Disease Models using the Systems Biology Markup Language", in *Simulation*, 95(10), 2019.

37. T. Nguyen, T. Jones, P. Fontanarrosa, J. Mante, Z. Zundel, D. Densmore, C. Myers, "Design of Asynchronous Genetic Circuits", in *Proceedings of the IEEE*, 107(7): 1356-1368, July 2019.

38. N. Roehner, B. Bartley, J. Beal. J. McLaughlin, M. Pocock, M. Zhang, Z. Zundel, and C. Myers, "Specifying Combinatorial Designs with the Synthetic Biology Open Language (SBOL)", in *ACS Synthetic Biology*, 8(7): 1519-1523, July 19, 2019.

39. G. Misirli, R. Taylor, A. Goni-Moreno, J. Mclaughlin, C. Myers, J. Gennari, P. Lord, A. Wipat, "SBOL-OWL: An ontological approach for formal and semantic representation of synthetic biology information", in *ACS Synthetic Biology*, 8(7): 1498-1514, July 19, 2019.

40. B. Bartley, K. Choi, M. Samineni, Z. Zundel, T. Nguyen, C. Myers, H. Sauro, "pySBOL: A Python Package for Genetic Design Automation and Standardization", in *ACS Synthetic Biology*, 8(7): 1515-1518, July 19, 2019.

41. L. Watanabe, T. Nguyen, M. Zhang, Z. Zundel, Z. Zhang, C. Madsen, N. Roehner, and C. Myers, "iBioSim 3: A Tool for Model-Based Genetic Circuit Design", in *ACS Synthetic Biology*, 8(7): 1560-1563, July 19, 2019.

42. G. Misirli, T. Nguyen, J. McLaughlin, P. Vaidyanathan, T. Jones, D. Densmore, C. Myers, and A. Wipat, "A Computational Workflow for the Automated Generation of Models of Genetic Designs", in *ACS Synthetic Biology*, 8(7): 1548-1559, July 19, 2019.

43. M. Neal, M Konig, D. Nickerson, G. Misirli, R. Kalbasi, A. Drager, K. Atalag, V. Chelliah, M. Cooling, D. Cook, S. Crook, M. de Alba, S. Friedman, A. Garny, J. Gennari, P. Gleeson, M. Golebiewski, M. Hucka, N. Juty, N. Le Novere, C. Myers, B. Olivier, H. Sauro, M. Scharm, J. Snoep, V. Toure, A. Wipat, O. Wolkenhauer, D. Waltemath, "Harmonizing semantic annotations for computational models in biology", in *Briefings in Bioinformatics*, 20(2): 540-550, March, 2019.

44. J. McLaughlin, C. Myers, Z. Zundel, N. Wilkinson, C. Atallah, and A. Wipat, "sboljs: Bringing the Synthetic Biology Open Language to the Web browser", in *ACS Synthetic Biology*, (8)1: 191-193, January 18, 2019.

45. F. Schreiber, G. Bader, P. Gleeson, M. Golebiewski, M. Hucka, S. Keating, N. Le Novère, C. Myers, D. Nickerson, B. Sommer, and D. Waltemath, "Specifications of Standards in Systems and Synthetic Biology: Status and Developments in 2017", in *Journal of Integrative Bioinformatics*, 15(1), April 2, 2018.

46. R. Cox, C. Madsen, J. McLaughlin, T. Nguyen, N. Roehner, B. Bartley, J. Beal, M. Bissell, K. Choi, K. Clancy, R. Grünberg, C. Macklin, G. Misirli, E. Oberortner, M. Pocock, M. Samineni, M. Zhang, Z. Zhang, Z. Zundel, J. Gennari, C. Myers, H. Sauro, and A. Wipat, "Synthetic Biology Open Language (SBOL) Version 2.2.0", in *Journal of Integrative Bioinformatics*, 15(1), April 2, 2018.

47. R. Cox, C. Madsen, J. McLaughlin, T. Nguyen, N. Roehner, B. Bartley, J. Beal, S. Bhatia, M.

Bissell, K. Clancy, T. Gorochowski, R. Grünberg, A. Luna, C. Myers, N. Le Novere, M. Pocock, H. Sauro, J. Sexton, G. Stan, J. Tabor, C. Voigt, Z. Zundel, and A. Wipat, "Synthetic Biology Open Language Visual (SBOL Visual) Version 2.0", in *Journal of Integrative Bioinformatics*, 15(1), April 2, 2018.

48. M. Hucka, F. Bergmann, A. Drager, S. Hoops, S. Keating, N. Le Novere, C. Myers, B. Olivier, S. Sahle, J. Schaff, L. Smith, D. Waltemath, and D. Wilkinson, "The Systems Biology Markup Language (SBML): Language Specification for Level 3 Version 2 Core", in *Journal of Integrative Bioinformatics*, 15(1), April 2, 2018.

49. J. McLaughlin, C. Myers, Z. Zundel, G. Misirli, M. Zhang, I. Ofiteru, A. Moreno, and A. Wipat, "SynBioHub: A Standards-Enabled Design Repository for Synthetic Biology", in *ACS Synthetic Biology*, 7(2): 682-688, February 16, 2018.

50. M. Zhang, J. McLaughlin, A. Wipat, and C. Myers, "SBOLDesigner 2: An Intuitive Tool for Structural Genetic Design", in *ACS Synthetic Biology*, 6(7): 1150-1160, July 21, 2017.

51. Z. Zundel, M. Samineni, Z. Zhang, and C. Myers, "A validator and Converter for the Synthetic Biology Open Language", in *ACS Synthetic Biology*, , 6(7): 1161-1168, July 21, 2017.

52. C. Myers, J. Beal, T. Gorochowski, H. Kuwahara, C. Madsen, J. McLaughlin, G. Misirli, T. Nguyen, E. Oberortner, M. Samineni, A. Wipat, M. Zhang, and Z. Zundel, "A Standard-Enabled Workflow for Synthetic Biology", in *Biochemical Society Transactions*, 45: 793-803, June 15, 2017.

53. H. Kuwahara, X. Cui, R. Umarov, R. Gruenberg, C. Myers, and X. Gao, "SBOLme: a Repository of SBOL Parts for Metabolic Engineering", in *ACS Synthetic Biology*, 6(4): 732-736, April 21, 2017.

54. J. Beal, R. Cox, R. Grunberg, J. McLaughlin, T. Nguyen, B. Bartley, M. Bissell, K. Choi, K. Clancy, C. Macklin, C. Madsen, G. Misirli, E. Oberortner, M. Pocock, N. Roehner, M. Samineni, M. Zhang, Z. Zhang, Z. Zundel, J. Gennari, C. Myers, H. Sauro and A. Wipat, "Synthetic Biology Open Language (SBOL) Version 2.1.0", *Journal of Integrative Bioinformatics*, 13(3):291, 2016.

55. F. Schreiber, G. Bader, P. Gleeson, M. Golebiewski, M. Hucka, B. Kornmeier, N. Le Novère, C. Myers, D. Nickerson, B. Sommer, and D. Waltemath, "Specifications of Standards in Systems and Synthetic Biology: Status and Developments in 2016", *Journal of Integrative Bioinformatics*, 13(3):289, 2016.

56. X. Li, C. Myers, and C. Kashyap, "Guest Editors' Introduction Challenges and Opportunities in Analog/Mixed-Signal CAD", in *IEEE Design & Test*, 33(5): 5-6, 2016.

57. V. Dubikhin, D. Sokolov, C. Myers, and A. Yakovlev, "Design of Mixed-signal Systems with Asynchronous Control", in *IEEE Design and Test*, 33(5): 44-55, 2016.

58. D. Waltemath, J. Karr, F. Bergmann, V. Chelliah, M. Hucka, M. Krantz, W. Liebermeister, P. Mendes, C. Myers, P. Pir, B. Alaybeyoglu, N. Aranganathan, K. Baghalian, A. Bittig, P. Burke, M. Cantarelli, Y. Chew, R. Costa, J. Cursons, T. Czauderna, A. Goldberg, H. Gómez, J. Hahn, T. Hameri, D. Kazakiewicz, I. Kiselev, V. Knight-Schrijver, C. Knüpfer, M. König, D. Lee, A. Lloret-Villas, N. Mandrik, J. Medley, B. Moreau, H. Meshkin, S. Palaniappan, D. Priego-Espinosa, M. Scharm, M. Sharma, K. Smallbone, N. Stanford, J. Song, T. Theile, M. Tokic, N. Tomar, V. Touré, J. Uhlendorf, T. Varusai, L. Watanabe, F. Wendland, M. Wolfien, J. Yurkovich, Y. Zhu, A. Zardilis, A. Zhukova, and F. Schreiber, "Toward community standards and software for whole-cell modeling", in *IEEE Trans. on Biomedical Engineering*, 63(10):2007-2014, 2016.

59. L. Watanabe and C. Myers, "Efficient Analysis of SBML Models of Cellular Populations Using Arrays", in *ACS Synthetic Biology*, 5(8): 835-841, 2016.

60. N. Roehner, J. Beal, K. Clancy, B. Bartley, G. Misirli, R. Grunberg, E. Oberortner, M. Pocock, M. Bissell, C. Madsen, T. Nguyen, M. Zhang, Z. Zhang, Z. Zundel, D. Densmore, J. Gennari, A. Wipat, H. Sauro, and C. Myers, "Sharing structure and function in biological design with SBOL 2.0", in *ACS Synthetic Biology*, 5(6): 498-506, 2016.

61. T. Nguyen, N. Roehner, Z. Zundel, and C. Myers, "A Converter from the Systems Biology Markup Language to the Synthetic Biology Open Language", in *ACS Synthetic Biology*, 5(6): 479-486, 2016.

62. Z. Zhang, W. Serwe, J. Wu, T. Yoneda, H. Zheng, and C. Myers, "An Improved Fault-Tolerant Routing Algorithm for a Network-on-Chip Derived with Formal Analysis", in *Science of Computer Programming*, 118: 24-39, 2016.

63. Z. Zhang, T. Nguyen, N. Roehner, G. Misirli, M. Pocock, E. Oberortner, M. Samineni, Z. Zundel, J. Beal, K. Clancy, A. Wipat, C. Myers, "libSBOLj 2.0: A Java Library to Support SBOL 2.0", in *IEEE Life Sciences Letters*, 1(4): 34-37, 2015.

64. C. Myers, "Computational Synthetic Biology: Progress and the Road Ahead", in *IEEE Transactions on Multi-scale Computing Systems*, 1(1): 19-32, 2015.

65. J. Quinn, R. Cox, A. Adler, J. Beal, S. Bhatia, Y. Cai, J. Chen, K. Clancy, M. Galdzicki, N. Hillson, N. Le Novère, A. Maheshwari, J. Alastair, C. Myers, Umesh P, M. Pocock, C. Rodriguez, L. Soldatova, G.-B. Stan, N. Swainston, A. Wipat, and H. Sauro, "SBOL Visual: A Graphical Language for Genetic Designs", in *PLOS Biology*, 13(12): e1002310, 2015.

66. N. Rodriguez, A. Thomas, L. Watanabe, I. Vazirabad, V. Kofia, H. Gómez, F. Mittag, J. Rudolph, F. Wrzodek, E. Netz, A. Diamantikos, J. Eichner, R. Keller, C. Wrzodek, S. Fröhlich, N. Lewis, C. Myers, N. Le Novère B. Palsson, M. Hucka, and A. Dräger, "JSBML 1.0: providing a smorgasbord of options to encode systems biology models", in *Bioinformatics*, 31(20):3383-6, 2015.

67. F. Schreiber, G. Bader, M. Golebiewski, M. Hucka, B. Kornmeier, N. Le Novère, C. Myers, D. Nickerson, B. Sommer, D. Waltemath and S. Weise, "Specifications of Standards in Systems and Synthetic Biology", *Journal of Integrative Bioinformatics*, 12(2):258, 2015.

68. L. Smith, M. Hucka, S. Hoops, A. Finney, M. Ginkel, C. Myers, I. Moraru and W. Liebermeister, "SBML Level 3 package: Hierarchical Model Composition, Version 1 Release 3", *Journal of Integrative Bioinformatics*, 12(2):268, 2015.

69. M. Hucka, F. Bergmann, A. Dräger, S. Hoops, S. Keating, N. Le Novère, C. Myers, B. Olivier, S. Sahle, J. Schaff, L. Smith, D. Waltemath and D. Wilkinson. "Systems Biology Markup Language (SBML) Level 2 Version 5: Structures and Facilities for Model Definitions", *Journal of Integrative Bioinformatics*, 12(2):271, 2015.

70. B. Bartley, J. Beal, K. Clancy, G. Misirli, N. Roehner, E. Oberortner, M. Pocock, M. Bissell, C. Madsen, T. Nguyen, Z. Zhang, J. Gennari, C. Myers, A. Wipat and H. Sauro. "Synthetic Biology Open Language (SBOL) Version 2.0.0", *Journal of Integrative Bioinformatics*, 12(2):272, 2015.

71. N. Roehner, Z. Zhang, T. Nguyen, C. Myers, "Generating Systems Biology Markup Language Models from the Synthetic Biology Open Language", in *ACS Synthetic Biology*, 4(8), 867-943, August 21, 2015.

72. M. Hucka, D. Nickerson, G. Bader, F. Bergmann, J. Cooper, E. Demir, A. Garny, M. Golebiewski, C. Myers, F. Schreiber, D. Waltemath, N. Le Novère, "Promoting coordinated development of community-based information standards for modeling in biology: the COMBINE initiative", in

Frontiers in Bioengineering and Biotechnology, 3(19), 2015.

73. H. Zheng, Z. Zhang, C. Myers, E. Rodriguez, and Y. Zhang, "Compositional Model Checking of Concurrent Systems", in *IEEE Transactions on Computers*, 64(6), June, 2015.

74. N. Roehner, E Oberortner, M. Pocock, J. Beal, K. Clancy, C. Madsen, G. Misirli, A. Wipat, H. Sauro, C. Myers, "A Proposed Data Model for the Next Version of the Synthetic Biology Open Language", in *ACS Synthetic Biology*, 4(1), 57-71, January 16, 2015.

75. C. Myers, H. Sauro, and A. Wipat, "Introduction to the Special Issue on Computational Synthetic Biology", in *ACM Journal on Emerging Technologies in Computing Systems*, 11(3), December, 2014.

76. C. Madsen, Z. Zhang, N. Roehner, C. Winstead, and C. Myers, "Stochastic Model Checking of Genetic Circuits", in *ACM Journal on Emerging Technologies in Computing Systems*, 11(3), December, 2014.

77. L. Watanabe and C. Myers, "Hierarchical Stochastic Simulation Algorithm for SBML Models of Genetic Circuits", in *Frontiers in Bioengineering and Biotechnology*, 2(55), 2014.

78. N. Roehner and C. J. Myers, "Directed Acyclic Graph-Based Technology Mapping of Genetic Circuit Models.", in *ACS Synthetic Biology*, 3(8), 543-555, August 15, 2014.

79. M. Galdzicki, K. Clancy, E. Oberortner, M. Pocock, J. Quinn, C. Rodriguez, N. Roehner, M. Wilson, L. Adam, J. C. Anderson, B. Bartley, J. Beal, D. Chandran, J. Chen, D. Densmore, D. Endy, R. Grunberg, J. Hallinan, N. Hillson, J. Johnson, A. Kuchinsky, M. Lux, G. Misirli, J. Peccoud, H. Plahar, E. Sirin, G.-B. Stan, A. Villalobos, A. Wipat, J. Gennari, C. Myers, H. Sauro, "SBOL: A community standard for communicating designs in synthetic biology", in *Nature Biotechnology*, 32(6): 545-550, June, 2014.

80. D. Waltemath, F. Bergmann, C. Chaouiya, T. Czauderna, P. Gleeson, C. Goble, M. Golebiewski, M. Hucka, N. Juty, O. Krebs, N. Le Novère, H. Mi, I. Moraru, C. Myers, D. Nickerson, B. Olivier, N. Rodriguez, F. Schreiber, L. Smith, F. Zhang, and E. Bonnet, "Meeting report from the fourth meeting of the Computational Modeling in Biology Network (COMBINE)", in *Standards in Genomic Sciences*, 9(3), 2014.

81. N. Roehner and C. J. Myers, "A Methodology to Annotate Systems Biology Markup Language Models with the Synthetic Biology Open Language", in *ACS Synthetic Biology*, 3(2): 57-66, Feb. 21, 2014.

82. J. Stevens and C. Myers, "Dynamic Modeling of Cellular Populations within iBioSim," in *ACS Synthetic Biology*, 2(5): 223-229, Nov. 21, 2012.

83. C. Madsen, C. Myers, T. Patterson, N. Roehner, J. Stevens, and C. Winstead, "Design and test of genetic circuits using iBioSim," in *IEEE Design and Test*, 29(3): 32-39, June, 2012.

84. S. Little, D. Walter, C. Myers, R. Thacker, S. Batchu, and T. Yoneda, "Verification of analog/mixed-signal circuits using labeled hybrid Petri nets," in *IEEE Transactions on CAD*, 30(4): 617-630, April, 2011.

85. N. Barker, C. Myers, and H. Kuwahara, "Learning genetic regulatory network connectivity from time series data," in *IEEE Transactions on Computational Biology and Bioinformatics*, 8(1):152-165, Jan-Mar, 2011.

86. N. Nguyen, C. Myers, H. Kuwahara, C. Winstead, and J. Keener, "Design and analysis of a robust genetic Muller C-element," in *Journal of Theoretical Biology*, 264(2): 174-187, May, 2010.

87. S. Little, D. Walter, K. Jones, C. Myers, and A. Sen, "Analog/mixed-signal circuit verification

using models generated from simulation traces," in *The International Journal of Foundations of Computer Science*, 21(2): 191-210, 2010.

88. H. Kuwahara, C. Myers, and M. Samoilov, "Temperature control of fimbriation circuit switch in uropathogenic *escherichia coli*: quantitative analysis via automated model abstraction," in *PLoS Computational Biology*, 6(3): e1000723, March 2010.

89. C. Myers, N. Barker, K. Jones, H. Kuwahara, C. Madsen, and N. Nguyen, "iBioSim: a tool for the analysis and design of genetic circuits," in *Bioinformatics*, 25(21): 2848-2849, November 1, 2009.

90. N. Hamada, Y. Shiga, T. Konishi, H. Saito, T. Yoneda, C. Myers, and T. Nanya, "A behavioral synthesis system for asynchronous circuits with bundled-data implementation," in *IPSJ Transactions on System LSI Design Methodology*, Vol. 2, 64-79, February, 2009.

91. D. Walter, S. Little, C. Myers, N. Seegmiller, and T. Yoneda, "Verification of analog/mixed-signal circuits using symbolic methods," in *IEEE Transactions on CAD*, 27(12): 2223-2235, December, 2008.

92. H. Kuwahara and C. Myers, "Production-Passage-Time Approximation: A New Approximation Method to Accelerate the Simulation Process of Enzymatic Reactions" in *Journal of Computational Biology*, 15(7): 779-792, September, 2008.

93. H. Saito, N. Hamada, N. Jindapetch, T. Yoneda, C. Myers, and T. Nanya, "Scheduling Methods for Asynchronous Circuits in Bundled-Data Implementation Based on the Approximation of Start Times," in *IEICE Transactions*, E90-A(12): 2790-2799, December, 2007.

94. T. Yoneda and C. Myers, "Synthesis of timed circuits based on decomposition," in *IEEE Transactions on CAD*, 26(7): 1177-1195, July 2007.

95. C. Nelson, C. Myers, and T. Yoneda, "Efficient verification of hazard-freedom in gate-level timed asynchronous circuits," in *IEEE Transactions on CAD*, 26(3): 592-605, March 2007.

96. H. Kuwahara, C. Myers, N. Barker, M. Samoilov, and A. Arkin, "Asynchronous abstraction methodology for genetic regulatory networks," in *Transactions on Computational Systems Biology VI*, LNBI 4220, 2006 (**Invited Paper**).

97. C. Myers, R. Harrison, D. Walter, N. Seegmiller, and S. Little, "The case for analog circuit verification," in *Elec. Notes in Theoretical Computer Science, 153(3): 53-63, June 20, 2006.*

98. H. Zheng, C. Myers, D. Walter, S. Little, and T. Yoneda, "Verification of timed circuits with failure directed abstractions," in *IEEE Transactions on CAD*, 25(3): 403-412, March, 2006.

99. T. Kitai, T. Yoneda, and C. Myers, "Failure trace analysis of timed circuits for automatic timing constraints derivation," *IEICE Transactions*, E88-D(11): 2555-2564, November, 2005.

100. D. Pradubsuwun, T. Yoneda, and C. Myers, "Partial order reduction for detecting safety and timing failures of timed circuits," in *IEICE Transactions*, E88-D(7): 1646-1661, July, 2005.

101. C. Winstead, J. Dai, S. Yu, C. Myers, R. Harrison, C. Schlegel, "CMOS analog MAP decoder for (8,4) Hamming code," in *IEEE Jour. of Solid State Circuits*, 39(1):122-131, January, 2004.

102. T. Kitai, Y. Oguro, T. Yoneda, E. Mercer, and C. Myers, "Partial order reduction for timed circuit verification based on a level oriented model," in *IEICE Transactions*, E86-D(12): 2601-2611, 2003.

103. H. Zheng, E. Mercer, and C. Myers, "Modular verification of timed circuits using automatic abstraction," in *IEEE Transactions on CAD* 22(9):1138-1153, September, 2003.

104. T. Yoneda, E. Mercer, and C. Myers, "Modular synthesis of timed circuits using partial order

reduction," in *IEICE Transactions*, E85-A(12): 2684-2692, 2002.

105. H. Jacobson and C. J. Myers, "Efficient algorithms for exact two-level hazard-free logic minimization," in *IEEE Transactions on CAD*, 21(11):1269-1283, November, 2002.

106. B. Zhou, T. Yoneda, and C. Myers, "Framework of timed trace theoretic verification revisited," in *IEICE Transactions*, E85-D(10): 1595-1604, 2002.

107. S. T. Jung and C. J. Myers, "Direct synthesis of timed circuits from free-choice STGs," in *IEEE Transactions on CAD*, 21(3): 275-290, March, 2002.

108. K. Stevens, S. Rotem, R. Ginosar, P. A. Beerel, C. J. Myers, K. Yun, R. Kol, C. Dike, and M. Roncken, "An Asynchronous Instruction Length Decoder," in *IEEE Journal of Solid State Circuits*, 36(2): 217-228, February, 2001.

109. W. Belluomini and C. J. Myers, "Timed circuit verification using TEL structures," in *IEEE Transactions on CAD*, 20(1):129-146, January, 2001.

110. A. E. Sjogren and C. J. Myers, "Interfacing synchronous and asynchronous modules within a high-speed pipeline," in *IEEE Transactions on VLSI Systems*, 8(5): 573-583, October, 2000.

111. W. Belluomini and C. J. Myers, "Timed state space exploration using POSETs," in *IEEE Transactions on CAD*, 19(5): 501-520, May, 2000.

112. C. J. Myers, T. G. Rokicki, and T. H.-Y. Meng, "POSET timing and its application to the synthesis and verification of gate-level timed circuits," in *IEEE Transactions on CAD*, 18(6): 769-786, June, 1999.

113. P. A. Beerel, C. J. Myers, and T. H.-Y. Meng, "Covering conditions and algorithms for the synthesis of speed-independent circuits", in *IEEE Transactions on CAD*, 17(3): 205-219, March, 1998.

114. C. J. Myers and T. H.-Y. Meng, "Synthesis of timed asynchronous circuits," in *IEEE Transactions on VLSI Systems*, 1(2): 106-119, June, 1993 (**Invited Paper**).

Patents

1. S. Rotem, P. Beerel, R. Ginosar, R. Kol, C. Myers, K. Stevens, and K. Yun, *Apparatus and method for parallel processing and self-timed marking of variable length instructions*, issued November 2, 1999, number 5,978,899.

2. S. Rotem, P. Beerel, R. Ginosar, R. Kol, C. Myers, K. Stevens, and K. Yun, *Apparatus and method for self-timed marking of variable length instructions having length-affecting prefix bytes*, issued September 7, 1999, number 5,948,096.

3. S. Rotem, P. Beerel, R. Ginosar, R. Kol, C. Myers, K. Stevens, and K. Yun, *Efficient self-timed marking of lengthy variable length instructions*, issued August 24, 1999, number 5,941,982.

4. S. Rotem, P. Beerel, R. Ginosar, R. Kol, C. Myers, K. Stevens, and K. Yun, *Branch instruction handling in a self-timed marking system*, issued August 3, 1999, number 5,931,944.

Refereed Conference and Workshop Papers

1. R. Roberts, T. Neupane, L. Buecherl, C. Myers, and Z. Zhang, "STAMINA 2.0: Improving Scalability of Infinite-State Stochastic Model Checking", in *23rd International Conference on Verification, Model Checking, and Abstract Interpretation, VMCAI 2022*, January, 2022.

2. T. Neupane, C. Myers, C. Madsen, Z. Zhang, and H. Zheng , "STAMINA: STochastic Approximate Model-checker for INfinite-state Analysis", in *Computer Aided Verification, CAV '19*, July, 2019.

3. C. Myers, G. Bader, P. Gleeson, M. Golebiewski, M. Hucka, N. Le Novere, D. Nickerson, F. Schreiber, and D. Waltemath, "A Brief History of COMBINE", in *2017 Winter Simulation Conference*, December, 2017.
4. V. Dubikhin, D. Sokolov, C. Myers, A. Mokhov, and, A. Yakovlev, "Model discovery for analog/mixed-signal circuits", in *2017 Frontiers in Analog CAD Workshop*, July, 2017.
5. V. Dubikhin, C. Myers, D. Sokolov, I. Syranidis, and, A. Yakovlev, "INVITED: Advances in Formal Methods for the Design of Analog/Mixed-Signal Systems", in *2017 Design Automation Conference*, June, 2017.
6. A. Fisher, C. Myers, and P. Li, "Reachability analysis using extremal rates", in 7^{th} *Nasa Formal Methods Symposium*, April, 2015.
7. Z. Zhang, W. Serwe, J. Wu, T. Yoneda, H. Zheng, and C. Myers, "Formal analysis of a fault-tolerant routing algorithm for a network-on-chip", in *19th International Workshop on Formal Methods for Industrial Critical Systems*, September, 2014.
8. A. Fisher, S. Batchu, K. Jones, D. Kulkarni, S. Little, D. Walter, and C. Myers, "LEMA: a tool for the formal verification of digitally-intensive analog/mixed-signal circuits," in *2014 Midwest Symposium on Circuits and Systems*, August, 2014.
9. F. Butzke, C. Myers, M. Moreira, and N. Calazans, "QDI logic for signaling data validity in bundled-data design: a kogge-stone case study," in *2014 International Symposium on Asynchronous Circuits and Systems*, May, 2014.
10. L. Watanabe and C. Myers, "Hierarchical stochastic simulation of genetic circuits," in *2014 Symposium on Theory of Modeling and Simulation*, April, 2014.
11. D. Kulkarni, A. Fisher, and C. Myers, "A new assertion property language for analog/mixed-signal circuits," in *2013 Forum on Design Languages*, September, 2013 (**best paper candidate**).
12. H. Lin, P. Li, and C. Myers, "Verification of digitally-intensive analog circuits via kernel ridge regression and hybrid reachability analysis," in *2013 Design Automation Conference*, June, 2013.
13. N. Miskov-Zivanov, J. Faeder, C. Myers, and H. Sauro, "Modeling and design automation of biological circuits and systems," in *2012 International Conference on Computer-Aided Design*, November, 2012 (**invited**).
14. H. Zheng, A. Price, and C. Myers, "Using decision diagrams to compactly represent state space for explicit model checking," in *2012 IEEE International High Level Design Validation and Test Workshop*, November, 2012.
15. Y. Zhang, E. Rodriguez, H. Zheng, and C. Myers, "An improvement in partial order reduction using behavioral analysis," in *IEEE Computer Society Annual Symp. on VLSI*, August, 2012.
16. H. Zheng, E. Rodriguez, Y. Zhang, and C. Myers, "A compositional minimization approach for large asynchronous design verification," in *19th International SPIN Workshop on Model Checking of Software*, July, 2012.
17. C. Madsen, C. Myers, N. Roehner, C. Winstead, and Z. Zhang, "Utilizing stochastic model checking to analyze genetic circuits," in *2012 Computational Intelligence in Bioinformatics and Computational Biology*, May, 2012 (**best student paper**).
18. D. Kulkarni, S. Batchu, and C. Myers, "Improved model generation of AMS circuits for formal verification," in *2011 Virtual Worldwide Forum for PhD Researchers in Electronic Design Automation*, November, 2011.

19. J. Wu, Z. Zhang, and C. Myers, "A fault-tolerant routing algorithm for a network-on-chip using a link fault model," in *2011 Virtual Worldwide Forum for PhD Researchers in Electronic Design Automation*, November, 2011.

20. H. Kuwahara and C. Myers, "Erlang-delayed stochastic chemical kinetic formalism for efficient analysis of biological systems with non-elementary reaction effects," in *2011 ACM Conference on Bioinformatics, Computational Biology, & Biomedicine*, August, 2011.

21. H. Yao, H. Zheng, and C. Myers, "State space reductions for scalable verification of asynchronous designs," in *2010 IEEE International High Level Design Validation and Test Workshop*, November, 2010.

22. C. Winstead, C. Madsen, and C. Myers, "iSSA: an incremental stochastic simulation algorithm for genetic circuits," in *2010 International Conference on Circuits and Systems*, May, 2010.

23. R. Thacker, K. Jones, C. Myers, and H. Zheng, "Automatic abstraction for verification of cyber-physical systems," in *The 1st ACM/IEEE International Conference on Cyber-Physical Systems*, April, 2010.

24. C. Myers, N. Barker, H. Kuwahara, K. Jones, C. Madsen, and N. Nguyen, "Genetic design automation," in *2009 International Conference on Computer-Aided Design*, November, 2009 (**invited**).

25. R. Thacker, C. Myers, K. Jones, and S. Little, "A new verification method for embedded systems," in *The 27th IEEE International Conference on Computer Design*, October, 2009

26. S. Little and C. Myers, "Abstract modeling and simulation aided verification of analog/mixed-signal circuits," in *Workshop on Formal Verification of Analog Circuits*, July, 2008.

27. N. Nguyen, N. Barker, H. Kuwahara, C. Madsen, and C. Myers, "Synthesis of genetic circuits from graphical specifications," in *2008 International Workshop on Logic Synthesis*, June, 2008 (**Invited**).

28. N. Hamada, Y. Shiga, H. Saito, T. Yoneda, C. Myers, and T. Nanya, "A behavioral synthesis method for asynchronous circuits with bundled-data implementation," in *The 8th International Conference on Application of Concurrency to System Design*, June, 2008.

29. S. Little, A. Sen, and C. Myers, "Application of automated model generation techniques to analog/mixed-signal circuits," in *8th International Workshop on Microprocessor Test and Verification*, December, 2007.

30. S. Little, D. Walter, K. Jones, and C. Myers, "Analog/mixed-signal circuit verification using models generated from simulation traces," in *Automated Technology for Verification and Analysis*, October, 2007.

31. D. Walter, S. Little, and C. Myers, "Bounded model checking of analog and mixed-signal circuits using an SMT solver," in *Automated Tech. for Verif. and Analysis*, October, 2007.

32. F. Beal, T. Yoneda, and C. Myers, "Hazard checking of timed asynchronous circuits revisited," in *7th International Conference on Applications of Concurrency to System Design*, July, 2007.

33. H. Kuwahara and C. Myers, "Production-passage-time approximation: a new approximation method to accelerate the simulation process of enzymatic reactions," in *The Eleventh Annual International Conference on Research in Computational Molecular Biology*, April, 2007.

34. N. Nguyen, H. Kuwahara, C. Myers, and J. Keener, "The design of a genetic Muller C-element," in *The Thirteenth International Symposium on Asynchronous Circuits and Systems*, March, 2007 (**Best Paper Award**).

35. D. Walter, S. Little, N. Seegmiller, C. Myers, and T. Yoneda, "Symbolic model checking of

analog/mixed-signal circuits", in *Asia and South Pacific Design Automation Conference 2007*, January, 2007.

36. S. Little, N. Seegmiller, D. Walter, C. Myers, and T. Yoneda, "Verification of analog/mixed-signal circuits using labeled hybrid Petri nets", in *2006 International Conference on Computer-Aided Design*, November, 2006.
37. T. Yoneda and C. Myers, "Effective contraction of timed STGs for decomposition based timed circuit synthesis," in *Automated Technology for Verification and Analysis*, pages 229-244, October, 2006.
38. H. Saito, N. Jindapetch, T. Yoneda, C. Myers, and T. Nanya, "ILP-based scheduling for asynchronous circuits in bundled-data implementation," in *2006 Int. Conf. on Computer and Information Technology*, September, 2006.
39. H. Kuwahara, C. Myers, and M. Samoilov, "Abstracted stochastic analysis of type 1 pili expression in e. coli," in *Proceedings of the 2006 International Conference on Bioinformatics & Computational Biology (BIOCOMP '06)*, pages 125-131, June, 2006.
40. N. Barker, C. Myers, and H. Kuwahara, "Learning genetic regulatory network connectivity from time series data," in *The 19th International Conference on Industrial, Engineering & Other Applications of Applied Intelligent Systems (IEA/AIE'06)*, June, 2006.
41. H. Saito, N. Jindapetch, T. Yoneda, C. Myers, and T. Nanya, "A scheduling method for asynchronous bundled-data implementations based on the completion of data operations," in *2005 Int. Tech. Conf. on Circuits/Systems, Computers and Communication*, July, 2005.
42. H. Saito, N. Jindapetch, T. Yoneda, and C. Myers, "A scheduling method for asynchronous bundled-data implementations," in *2005 Int. Workshop on Logic Synthesis*, June, 2005.
43. H. Kuwahara, C. Myers, N. Barker, M. Samoilov, and A. Arkin, "Asynchronous abstraction methodology for genetic regulatory networks," in *Third International Workshop on Computational Methods in Systems Biology*, April, 2005.
44. C. Myers, R. Harrison, D. Walter, N. Seegmiller, and S. Little, "The case for analog circuit verification," in *Workshop on Formal Verification of Analog Circuits*, April, 2005.
45. T. Yoneda, A. Matsumoto, M. Kato, and C. J. Myers, "High level synthesis of timed asynchronous circuits," in *The Eleventh International Symposium on Asynchronous Circuits and Systems*, pages 210-218, March, 2005.
46. S. Little, D. Walter, N. Seegmiller, C. Myers, and T. Yoneda, "Verification of analog and mixed-signal circuits using timed hybrid Petri nets," in *Automated Technology for Verification and Analysis*, pages 426-440, November, 2004.
47. D. Pradubsuwun, T. Yoneda, and C. Myers, "Partial order reduction for detecting safety and timing failures of timed circuits," in *Automated Technology for Verification and Analysis*, pages 339-353, November, 2004.
48. T. Yoneda, H. Onda, and C. J. Myers, "Synthesis of speed independent circuits based on decomposition," in *The Tenth International Symposium on Asynchronous Circuits and Systems*, pages 135-145, April, 2004.
49. C. Nelson, C. Myers, and T. Yoneda, "Efficient verification of hazard-freedom in gate-level timed asynchronous circuits", in *2003 International Conference on Computer-Aided Design*, pages 424-431, November, 2003.
50. H. Zheng, C. Myers, D. Walter, S. Little, and T. Yoneda, "Verification of timed circuits with failure directed abstractions", in *IEEE International Conference on Computer Design*, pages

28-35, October, 2003.

51. C. Myers, E. Mercer, and H. Jacobson, "Verifying synchronization strategies", in *Formal Methods for Globally Asynchronous Locally Synchronous (GALS) Architecture*, September, 2003 (**Invited paper**).

52. T. Kitai, Y. Oguro, T. Yoneda, E. Mercer, and C. Myers, "Level oriented formal model for asynchronous circuit verification and its efficient analysis method," in *2002 Pacific Rim International Symposium on Dependable Computing*, pages 210-218, November, 2002.

53. T. Yoneda, T. Kitai, and C. Myers, "Automatic derivation of timing constraints by failure analysis," in *Computer Aided Verification, CAV '02*, pages 195-208, July, 2002.

54. C. Winstead, J. Dai, S. Yu, R. Harrison, C. Myers, and C. Schlegel, "Analog decoding of product codes" in *International Symposium on Information Theory*, June, 2002.

55. J. Dai, C. J. Winstead, C. J. Myers, R. R. Harrison, and C. Schlegel, "Cell library for automatic synthesis of analog error control decoders," in *2002 International Conference on Circuits and Systems*, volume 4, pages 481-484, May, 2002.

56. E. Mercer, C. J. Myers, T. Yoneda, and H. Zheng, "Modular synthesis of timed circuits using partial orders on LPNs," in *Theory and Practice of Timed Systems, TPTS '02*, April, 2002.

57. H. Jacobson, P. Kudva, P. Bose, P. Cook, S. Schuster, E. Mercer, and C. J. Myers, "Synchronous interlocked pipelines," in *The Eighth International Symposium on Asynchronous Circuits and Systems*, pages 3-12, April, 2002.

58. B. Zhou, T. Yoneda, and C. Myers "Framework of timed trace theoretic verification revisited," in *The Tenth Asian Test Symposium, ATS '01*, pages 437-442, November, 2001.

59. T. Yoneda, E. Mercer, and C. Myers, "Modular synthesis of timed circuits using partial order reduction" in *The Tenth Workshop on Synthesis and System Integration of MIxed Technologies (SASIMI 2001)*, October, 2001.

60. E. Mercer, C. Myers, and T. Yoneda, "Improved POSET timing analysis in timed Petri nets" in *The Tenth Workshop on Synthesis and System Integration of MIxed Technologies (SASIMI 2001)*, October, 2001.

61. C. Winstead, C. Myers, C. Schlegel, and R. Harrison, "Analog decoding of product codes" in *2001 IEEE Information Theory Workshop*, pages 131-133, September, 2001.

62. H. Zheng, E. Mercer, and C. J. Myers "Automatic abstraction for verification of timed circuits and systems," in *Computer Aided Verification, CAV '01*, pages 182-193, July, 2001.

63. C. Winstead, J. Dai, W. J. Kim, S. Little, Y.-B. Kim, C. Myers, and C. Schlegel, "Analog MAP Decoder for (8,4) Hamming code in subthreshold CMOS" in *2001 IEEE International Symposium on Information Theory*, page 330, June, 2001.

64. K. Killpack, E. Mercer, C. J. Myers, "A standard-cell self-timed multiplier for power and area critical synchronous systems" in *2001 Advanced Research in VLSI Conference*, pages 188-201, March, 2001.

65. C. Winstead, J. Dai, W. J. Kim, S. Little, Y.-B. Kim, C. Myers, and C. Schlegel, "Analog MAP Decoder for (8,4) Hamming code in subthreshold CMOS" in *2001 Advanced Research in VLSI Conference*, pages 132-147, March, 2001.

66. C. J. Myers and H. Jacobson, "Efficient exact two-level hazard-free logic minimization," in *The Seventh International Symposium on Asynchronous Circuits and Systems*, pages 64-73, March, 2001 **(Best Paper Finalist)**.

67. C. J. Myers, W. Belluomini, K. Killpack, E. Mercer, E. Peskin, and H. Zheng, "Timed circuits: a new paradigm for high-speed design", in *Asia and South Pacific Design Automation Conference 2001*, pages 335-340, February, 2001 **(Invited Paper)**.

68. H. Zheng and C. J. Myers, "Automatic abstraction for synthesis and verification of deterministic timed systems" in *2000 ACM/IEEE International Workshop on Timing Issues in the Specification and Synthesis of Digital Systems*, December, 2000.

69. H. Jacobson, C. Myers, and G. Gopalakrishnan, "Achieving fast and exact hazard-free logic minimization of extended burst-mode gC finite state machines" in *2000 International Conference on Computer-Aided Design*, pages 303-310, November, 2000.

70. E. Mercer and C. J. Myers, "Stochastic cycle period analysis of timed circuits," in *2000 International Conference on Circuits and Systems*, volume II, pages 172-175, May, 2000.

71. S. T. Jung and C. J. Myers, "Direct synthesis of timed asynchronous circuits," in *1999 International Conference on Computer-Aided Design*, pages 332-337, November, 1999.

72. B. Bachman, H. Zheng, and C. Myers, "Architectural synthesis of timed asynchronous systems", in *IEEE International Conference on Computer Design*, pages 354-363, October, 1999.

73. E. Mercer and C. J. Myers, "Stochastic cycle period analysis of timed circuits," in *1999 International Workshop on Logic Synthesis*, June, 1999.

74. S. T. Jung and C. J. Myers, "Direct synthesis of timed asynchronous circuits," in *1999 International Workshop on Logic Synthesis*, June, 1999.

75. S. Rotem, K. Stevens, R. Ginosar, P. A. Beerel, C. J. Myers, K. Y. Yun, R. Kol, C. Dike, M. Roncken, and B. Agapiev, "RAPPID: An Asynchronous Instruction Length Decoder," in *The Fifth International Symposium on Advanced Research in Asynchronous Circuits and Systems*, pages 60-70, April, 1999 (**Best Paper Award**).

76. W. Belluomini, C. J. Myers, and H. P., Hofstee, "Verification of Delayed-Reset Domino Circuits Using ATACS," in *The Fifth International Symposium on Advanced Research in Asynchronous Circuits and Systems*, pages 3-12, April, 1999.

77. W. Belluomini, C. J. Myers, and H. P., Hofstee, "Verification of Delayed-Reset Domino Circuits Using ATACS," in *1999 ACM/IEEE International Workshop on Timing Issues in the Specification and Synthesis of Digital Systems*, March, 1999.

78. R. A. Thacker, W. Belluomini, and C. J. Myers, "Timed circuit synthesis using implicit methods," in *1999 12th VLSI Design Conference*, pages 181-188, January, 1999.

79. W. Belluomini and C. J. Myers "Verification of timed systems using POSETs," in *Computer Aided Verification, CAV '98*, pages 403-415, June, 1998.

80. W. Chou, P. Beerel, R. Ginosar, R. Kol, C. Myers, S. Rotem, K. Stevens, and K. Yun, "Average-case optimized technology mapping of one-hot domino circuits," in *The Fourth International Symposium on Advanced Research in Asynchronous Circuits and Systems*, pages 80-91, April, 1998.

81. W. Belluomini and C. J. Myers, "Timed Event/Level Structures," in *1997 ACM/IEEE International Workshop on Timing Issues in the Specification and Synthesis of Digital Systems*, December, 1997.

82. A. E. Sjogren and C. J. Myers, "Interfacing synchronous and asynchronous modules within a high-speed pipeline," in *17th Conf. on Advanced Research in VLSI*, pages 47-61, Sept, 1997.

83. R. A. Thacker and C. J. Myers, "Synthesis of timed circuits using BDDs," in *1997 International Workshop on Logic Synthesis*, May, 1997.

84. C. J. Myers and H. Zheng, "An asynchronous implementations of the MAXLIST algorithm," in *1997 International Conference on Acoustics, Speech, and Signal Processing*, volume 1, pages 647-650, April, 1997.
85. W. Belluomini and C. J. Myers, "Efficient timing analysis algorithms for timed state space exploration," in *The Third International Symposium on Advanced Research in Asynchronous Circuits and Systems*, pages 88-100, April, 1997.
86. C. J. Myers, P. A. Beerel, and T. H.-Y. Meng, "Technology mapping of timed circuits," in *2nd Working Conference on Asynchronous Design Methodologies*, pages 138-147, June, 1995.
87. C. J. Myers, T. G. Rokicki, and T. H.-Y. Meng, "Automatic synthesis of gate-level timed circuits with choice," in *1995 Chapel Hill Conference on Advanced Research in VLSI*, pages 42-58, March, 1995.
88. T. G. Rokicki and C. J. Myers "Automatic verification of timed circuits," in *Computer Aided Verification, CAV '94*, pages 468-480, June, 1994.
89. C. J. Myers and T. H.-Y. Meng, "Synthesis of timed asynchronous circuits," in *IEEE International Conference on Computer Design, ICCD-1992*, pages 279-284, October, 1992.

Invited Talks

1. University of Rostock, Rostock, Germany, August 2022
2. Israeli SynBio Association Inaugural Conference, Herzliya, Israel, June 2022
3. SEED Conference Tutorial, Washington DC, April 2022
4. University of Minnesota, Virtual, December 2021
5. NSF Workshop on Systems and Control in Synthetic Biology, Washington DC, November 2021
6. SEED Conference, Virtual, June 2021
7. COMBINE Forum, Virtual, October 2020
8. 2020 International Workshop on BioDesign Automation, Virtual, July 2020
9. DARPA SD2 PI Meeting, Santa Barbara, CA, January 2020
10. Interagency Synthetic Biology Meeting, Washington D.C., October 2019
11. University of Cambridge, Cambridge, United Kingdom, October 2019
12. Shonan, Tokyo, Japan, September 2019
13. COMBINE Forum, Heidelberg, Germany, July 2019
14. Imperial College London, London, United Kingdom, July 2019
15. Oxford Synthetic Biology CDT Symposium, Oxford, United Kingdom, July 2019
16. BioRoboost, Madrid, Spain, June 2019
17. Oxford Global Synthetic Biology Congress, London, United Kingdon, November 2018
18. SB4D Workshop, Arlington, VA, September 2018
19. CMSB 2018, Brno, Czech Republic, September 2018
20. University College London, London, United Kingdom, September 2018
21. NSF Living Computing Project Site Visit, Boston, MA, August 2018
22. SBOL Developers Workshop, Berkeley, CA, July 2018
23. DARPA SD2 PI Meeting, Seattle, WA, July 2018
24. ASYNC 2018, Vienna, Austria, May 2018
25. Dagstuhl Seminar, Germany, March 2018
26. University of Nebraska, Lincoln, NE, October 2017
27. Technical University of Denmark, Copenhagen, Denmark, October 2017

28. SBOL Developers Workshop, Pittsburgh, PA, August 2017
29. SEED Conference, Vancouver, Canada, June 2017
30. Massachusetts Instistute of Technology, Cambridge, MA, May 2017
31. Boston University, Boston, MA, May 2017
32. BBN/Raytheon, Cambridge, MA, April 2017
33. Microsoft Research, Cambridge, UK, March 2017
34. Newcastle University, Newcastle, UK, March 2017
35. University of Oxford, Oxford, UK, March 2017
36. University of Bristol, Bristol, UK, February 2017
37. Imperial College London, London, UK, February 2017
38. SynBioUK 2016, Edinburgh, UK, November 2016
39. University of Oxford, Oxford, UK, November 2016
40. Newcastle University, Newcastle, UK, October 2016
41. COMBINE Workshop, Newcastle, UK, September 2016
42. SBOL Workshop, Newcastle, UK, August 2016
43. HARMONY Workshop, Auckland, New Zealand, June 2016
44. IWBDA, Seattle, WA, August 2015
45. VEMDP Workshop, San Francisco, CA, July 2015
46. Verimag, Grenoble, France, June 2015
47. Inria, Grenoble, France, June 2015
48. HARMONY Workshop, Wittenberg, Germany, April 2015
49. SY-BIO Workshop, Dallas, Texas, March 2015
50. RWTH Aachen, Aachen, Germany, March 2015
51. Whole Cell Summer School, Rostock, Germany, March 2015
52. ICSB Workshop on Standards, Melbourne, Australia, September 2014
53. ICSB Tutorial, Melbourne, Australia, September 2014
54. COMBINE Workshop, Los Angeles, CA, August 2014
55. SEED Conference, Los Angeles, CA, July 2014
56. Inria, Grenoble, France, July 2014
57. HARMONY Workshop, Manchester, UK, April 2014
58. University of California, Davis, CA, January 2014
59. COMBINE Workshop, Paris, France, September 2013
60. International Workshop on Bio-Design Automation, London, UK, July 2013
61. Synthetic Biology 6.0, London, UK, July 2013
62. Design Automation Summer School, Austin, TX, June 2013
63. University of Connecticut Health Science Center, Farmington, CT, May 2013
64. Newcastle University, Newcastle, UK, April 2013
65. University of South Florida, Tampa, CA, April 2013
66. National Tsing Hua University, Hsinchu, Taiwan, March 2013
67. National Taiwan University, Genome and Systems Biology Center, Taipei, Taiwan, March 2013
68. Academia Sinica, Nankang, Taiwan, March 2013
69. National Taiwan University, EECS, Taipei, Taiwan, March 2013
70. Workshop on Design Automation for AMS, San Jose, CA, November 2012

71. ICCAD, San Jose, CA, November 2012
72. COMBINE Workshop, Toronto, Canada, August 2012
73. CAV Tutorial, Berkeley, CA, July 2012
74. RWTH Aachen, Aachen, Germany, May 2012
75. Cornell University, Ithaca, NY, April 2012
76. Intel Corporation, Portland, OR, December 2011
77. COMBINE Workshop, Heidelberg, Germany, September 2011
78. International Conference on Systems Biology Tutorial, Heidelberg, Germany, September 2011
79. Design Automation Summer School, San Diego, CA, June 2011
80. Workshop on Diversity in Design Automation and Test, Pittsburgh, PA, May 2011
81. Dagstuhl Seminar, Germany, April 2011
82. SRC Verification Review, Santa Barbara, CA, April 2011
83. Microsoft Research Cambridge, United Kingdom, October 2010
84. Intel Corporation, Portland, OR, July 2010
85. Dagstuhl Seminar, Germany, July 2010
86. Carnegie Mellon University, Pittsburgh, PA, December 2009
87. Brigham Young University, Provo, UT, November 2009
88. ICCAD Tutorial, San Jose, CA, November 2009
89. CANDE Workshop, Monterey, CA, October 2009
90. Intel Corporation, Portland, OR, October 2009
91. First RoSBNet Synthetic Biology Workshop, Oxford, United Kingdom, September 2009
92. Technical University of Denmark, August 2009
93. Utah State University, Logan, UT, July 2009
94. International Workshop on Bio-Design Automation, San Francisco, CA, July 2009
95. NSF Workshop: EDA-Past, Present, and Future, Washington D.C., July 2009
96. SRC Verification Review, Raleigh, NC, April 2009
97. Caltech, March 2009
98. University of California, Berkeley, October 2008
99. Technical University of Kaiserslautern, Germany, September 2008
100. Dagstuhl Seminar, Germany, September 2008
101. International Conference on Systems Biology Tutorial, Gothenburg, Swedwn, August 2008
102. International Workshop on Logic Synthesis, Lake Tahoe, CA, June 2008
103. IMA Workshop, Minneapolis, MN, April 2008
104. SRC Verification Review, Berkeley, CA, March 2008
105. Banbury Workshop, Cold Spring Harbor, NY, May 2007
106. SRC Verification Review, Salt Lake City, UT, April 2007
107. University of California, Berkeley, CA, March 2007
108. Cadence Berkeley Labs, Berkeley, CA, March 2007
109. EPFL, Laussane, Switzerland, June 2006
110. NSF/ECS Grantees Workship, Tuskegee, AL, June 2006
111. University of Southern California, May 2006
112. SRC Verification Review, Pittsburgh, PA, April 2006
113. David Evans Conference on CE, Heber City, UT, May 2005

114. Caltech, May 2005
115. SRC Verification Review, Boulder, CO, March 2005
116. Southern Taiwan University of Technology, Tainan, Taiwan, November 2004
117. Brigham Young University, November 2004
118. Kitano Symbiotic Systems Group, Tokyo, June 2004
119. SRC Verification Review, Princeton, NJ, March 2004
120. BioMath Seminar, U. of Utah, December 2003
121. Bioengineering Seminar, U. of Utah, September 2003
122. FMGALS 2003, September 2003
123. Human Genome Center, U. of Tokyo, May 2003
124. Research Center for Advanced Science and Technology, U. of Tokyo, May 2003
125. SRC Verification Review, Austin, TX, April 2003
126. UC Berkeley, February 2003
127. Stanford University, February 2003
128. David Evans Conference on CE, Heber City, UT, June 2002
129. SRC Verification Review, Salt Lake City, UT, March 2002
130. SRC Verification Review, Pittsburgh, PA, March 2001
131. ASP-DAC 2001, February 2001
132. Lucent Technologies, November 2000
133. Sonic Innovations, September 2000
134. SUN Microsystems, June 2000
135. SRC Verification Review, Austin, TX, March 2000
136. Caltech, March 2000
137. Asynchronous design tutorial for Samsung, July 1999
138. SRC CAD Review, Irvine, CA, March 1999
139. ASYNC98, CAD demo, San Diego, CA, April 1998
140. SRC CAD Review, Austin, TX, March 1998
141. IBM Austin Research Laboratory, June 1997
142. Stanford University, May 1997
143. Technical University of Denmark, April 1997
144. University of Manchester, April 1997
145. ASYNC97, CAD demo, Eindhoven, Netherlands, April 1997
146. Intel Portland, August 1996
147. Caltech, May 1996
148. 3-day short course on asynchronous design, Intel Portland, April 1996
149. ASYNC96, CAD demo, Aizu, Japan, March 1996
150. Beijing Institute of Machinery, March 1996
151. Intel Portland, September 1995
152. Intel Israel, August 1995
153. Phillips Nat Lab in Eindhoven, Netherlands, June 1995
154. IMEC in Leuven, Belgium, June 1995
155. University of Manchester, June 1995
156. SUN Microsystems, April 1995